



ISMC-01-1584

October 22, 2003

To: Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/623,912 07/21/03

Yue-der Chih et al.

MEMORY ARRAY WITH BYTE-ALTERABLE  
CAPABILITY

Grp. Art Unit: \_\_\_\_\_

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.

The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56. Copies of each document is included herewith.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on October 23, 2003.

Stephen B. Ackerman, Reg.# 37761

Signature/Date Stephen B. Ackerman 10/23/03

U.S. Patent 5,812,452 to Hoang, "Electrically Byte-Selectable and Byte-Alterable Memory Arrays," describes a memory array which utilized a byte (block) select transistor to allow the memory cells to be accessed and altered on a byte basis.

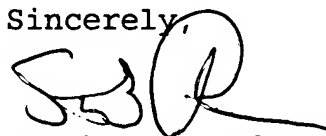
U.S. Patent 6,201,732 to Caywood, "Low Voltage Single CMOS Electrically Erasable Read-Only Memory," describes a CMOS memory cell which can be programmed, erased and operate at low voltages.

U.S. Patent 6,128,220 to Banyai et al., "Apparatus for Enabling EEPROM Functionality Using a Flash Memory Device," discloses a byte-alterable non-volatile memory.

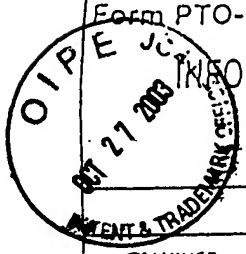
U.S. Patent 6,088,269 to Lambertson, "Compact Page-Erasable EEPROM Non-Volatile Memory," discloses a page erasable memory which uses two layers of conductive or semiconductive material.

U.S. Patent 6,212,102 to Georgakos et al., "EEPROM and Method for Triggering the EEPROM," discloses a programmable memory which uses source side selection.

Sincerely,

A handwritten signature in black ink, appearing to read 'SBA', with a large, stylized loop at the end.

Stephen B. Ackerman,  
Reg. No. 37761



Form PTO-1449	Doc No (Number) (Sequence) <b>TSmc-01-1584</b>	Application Number <b>10/623,912</b>
INFORMATION DISCLOSURE CITATION IN AN APPLICATION <small>(Use several sheets if necessary)</small>	Applicant <b>Yue-der Chih et al.</b>	
	Filing Date <b>07/21/03</b>	Drawn Art Unit

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
	5812452	9/22/98	Hoang	365	185.11	6/30/97
	6201732	3/13/01	Caywood	365	185.05	2/2/00
	6128220	10/3/00	Banyai et al.	365	185.11	1/21/99
	6088269	7/11/00	Lambertson	365	185.28	11/6/98
	6212102	4/3/01	Georgakos et al.	365	185.18	2/12/99

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
					YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)


EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant